

10mΩ/45V, High Performance Synchronous Rectifier IC

Product Description

DK5V45R10S is a simple but high efficiency synchronous rectifier solution with only two function pins A-K, corresponding A-K pins of Schottky diode. With 45V power NMOS integrated, DK5V45R10S can significantly reduce conduction loss of traditional Schottky diode. Hence the overall efficiency can be improved. The package of DK5V45R10S is SM-7(compatible with TO-277).

Product Feature

Applications

USB Charger

Adapter

etc

LED Driver

- Suitable for PSR/SSR feedback mode
- Ultra low V_F, ultra low temperature rise
- CCM/QR/DCM flyback mode support
- 45V/10mΩ NMOS integrated
- Positive/negative rectification support

- Patented self power supply, no need of external power supply component
- Self ON/OFF status detection, no need of external sync signal
- Improve EMC/EMI effect
- Replace Schottky Barrier Diode directly

<u>PINS</u>



Pins Function

| No. | Name | Description |
|-----|------|--------------------------------------|
| 1 | К | Same as diode cathode in application |
| 2 | A | Same as diode anode in application |

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Typical Power Range

| Model No. | Input Voltage | Typical Output Power | | | |
|------------|---------------|----------------------|--|--|--|
| DK5V45R10S | 85-265V AC | 5V 41A | | | |

Remarks: The typical power is tested at 45 $\,\,^\circ \! C\,$ in closed environment, and the rated output current of is recommended not to exceed 4.1A

Functional Structure Diagram



Absolute Maximum Rating

| Parameter | Symbol | Min | Тур | Max | Unit |
|---------------------------|----------------------|-----|-----|-----|------|
| NMOS Breakdown Voltage | V _{(BR)DSS} | 45 | | | V |
| NMOS MAX. Average current | ID | | | 45 | А |
| NMOS Peak current | I _{DS} | | | 60 | А |
| SM-7 Dissipation power | P _{DMAX} | | 1 | | W |

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DK5V45R10S

| Thermal Resistance (junction to ambient) | Rθ _{JA} | | 76 | | °C /W |
|--|------------------|-----|--------|-----|--------------|
| Thermal Resistance (junction to tube) | Rθ _{JC} | | 4 | | °C /W |
| Case/Working Temperature Range | Tc | | | | °C |
| Storage Temperature Range | T _{STG} | -55 | | 155 | °C |
| Junction Temperature | TJ | -40 | | 150 | °C |
| Soldering Temperature | | | 260/55 | | °C |

<u>Electrical Characteristics</u> (T_A = 25°C)

| Parameter | Symbol | Condition | Min | Тур | Max | Unit |
|--|----------------------|-----------|------|------|------|------|
| Power Supply Section | | | | | | |
| V_{CC} Start-up Voltage ^① | V_{CC_ON} | | | 7.2 | | V |
| Undervoltage Protection Threshold $^{(1)}$ | V_{CC_uvlo} | | | 3.3 | | V |
| Overvoltage Protection Threshold $^{(1)}$ | V _{OVP} | | | 11 | | V |
| Smart Detection and Control Section | | | | | | |
| NMOS Turn-on Voltage | V _{ON} | K voltage | -210 | -213 | -216 | mV |
| NMOS Turn-on Delay | T _{DON} | | | | 150 | ns |
| NMOS Turn-off Delay | T _{DOFF} | | | | 50 | ns |
| NMOS Max. Conduction Duration | T _{ON_MAX} | | | 20 | 25 | μs |
| NMOS Min. Conduction Duration | T _{ON_MIN} | | 166 | 197 | 222 | ns |
| NMOS Min. Turn-off Duration | T _{OFF_MIN} | | 495 | 596 | 686 | ns |
| Deadtime Duration ^② | T _D | | | 450 | | ns |
| Max | F _{S_MAX} | | | | 150 | KHz |
| NMOS Section | | | | | | |
| NMOS Conduction Resistance | R _{DS_ON} | | | | 10 | mΩ |

Remarks: (1). Voltages in specification are referenced at pin A.

(2). No Deadtime duration, no NMOS Max. Conduction Duration.

Operation Principle

Patented self power supply

DK5V45R10S series has patented built in capacitor for energy storage and self power supply circuit, which can meet the demand of control circuit and the drive of MOSFET, no need of external power supply circuit and energy storage component.

• Start up

When K point has a higher voltage than A point, Vcc voltage will gradually increased through self power supply circuit. When Vcc voltage is lower than starting voltage Vcc_on, the built-in power MOSFET is closed. When Vcc voltage exceed Vcc_on, the starting status finishes. When Vcc voltage falls below reset



voltage Vcc_uvlo, the synchronous rectifier IC re-enters starting status.

Power MOSFET control

When the circuit detects the voltage over A-K is higher than the turn-on voltage Von, the power MOSFET will be turned on. The control circuit can real time monitoring K point voltage variation. According to the change of charge and discharge time of previous cycle, control circuit can deduce the turn-on time of

power MOSFET of current cycle. When the turn-on time of the power MOSFET reaches Ton, or when the current flows through the MOSFET is detected reduced to 0, power MOSFET is turned off.

• <u>RC Snubber</u>

During start-up, output short circuit, input over voltage, continuous current mode may induce spike voltage across the power MOSFET. To prevent built-in power MOSFET avalanche breakdown due to over voltage, a RC snubber can be introduced across A-K to reduce the spike voltage.

· Conduction Resistance

During operating process, the conduction resistance increases with the temperature rising, the efficiency will fall as a result. Increase the heat dissipation area appropriately can reduce the working temperature of the synchronous rectifier IC.

· Caution!

Make sure the working voltage of the IC is lower than its breakdown voltage (V_{BVDSS}). Make sure the IC works with the operating range, specially the working temperature could not be higher than Operating Junction Temperature.



Typical Application

1. High side





Package and Packing Information

1. SM-7 Package





2. Packing Information: 5K/reel





| Symbol | A0 | B0 | ко | P0 | P1 | w |
|-----------|----------|---------|----------|----------|----------|-----------|
| SPEC (mm) | 4.30±0.1 | 6.8±0.1 | 1.4±0.1 | 4.00±0.1 | 8.00±0.1 | 16.00±0.3 |
| Symbol | P2 | т | F | F | ПО | D1 |
| • • • | • • | • | L | | 5 | 01 |



3. Welding Specification Reference





Caution: This product is a static sensitive component, please pay a attention to protect! The scope of ESD damage can be extended from minor performance to equipment failure. Precision IC may be damaged, which may result in component parameters not meeting the published specifications.

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