LIS3DSH State Machine

Version 1.2





Educational part: What is state machine? How it works?

LIS3DSH overview and state machines features (structure, parametres, etc.)

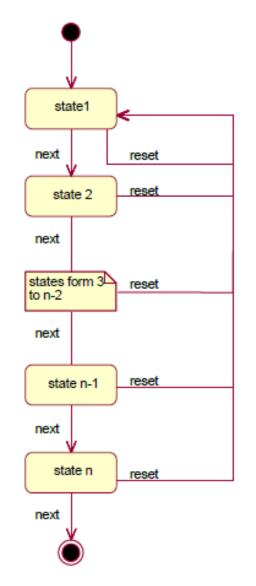
UNICO: PC GUI with debug support for the LIS3DSH state machine

LIS3DSH state machine examples, Live Demo



What is a state machine 3

- State Machine (SM) is a set of defined states, with inputs, outputs and transitions between states.
- The machine is in only one state at a time; the state it is in at any given time is called the current state.
- It can change from one state to another when a triggering event or condition occurs, this is called a transition.





LIS3DSH 4

- 3-Axis Digital SPI/I2C Accelerometer
- 5 selectable Full Scales: 2, 4, 6, 8, 16g
- 2 programmable embedded finite-state machines for interrupt generation
- Very High Resolution (up to 14 bit) and low noise (150µg/vHz)
- Low power consumption: 11µA in Active mode (3.1Hz) and 2μ A in Power down mode
- High Flexibility
- P2Pcompatible with LIS3DH





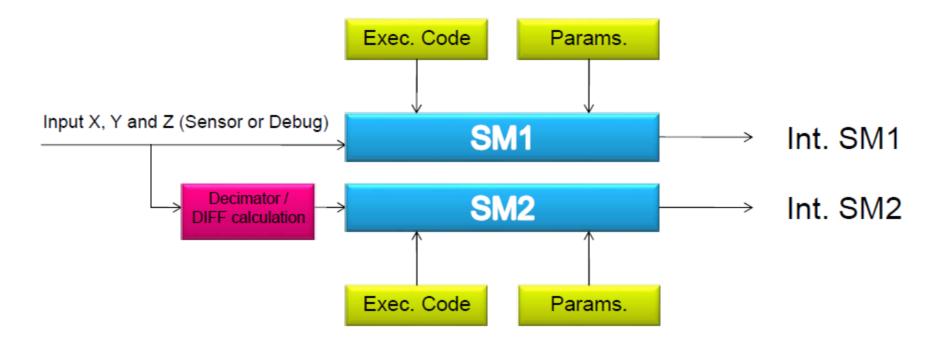
State Machines of LIS3DSH

- LIS3DSH has two independently configurable State Machines(SM)
- Each SM has up to 16 states
- SM can run once or can be continuously running (looping)
- SM1 and SM2 can run independently or synchronized but with same input data
- SM1 is performed first
- Input data are 8-bit wide



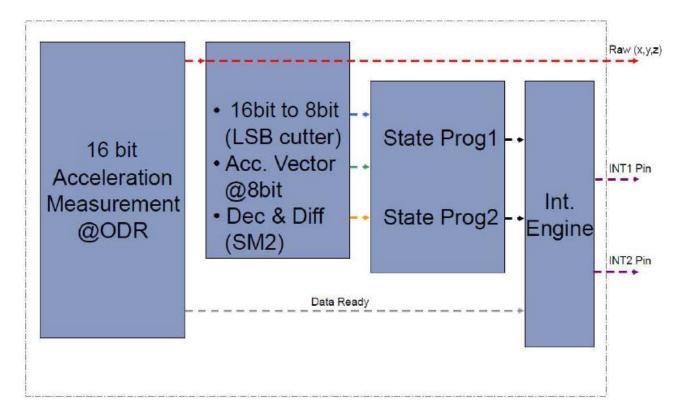
• State-machines are identical with some exceptions:

- State Program #2 has decimator functionality
- State Program #2 has DIFF functionality (new data Vs previous data, new data Vs constant)





• LIS3DSH data flow:





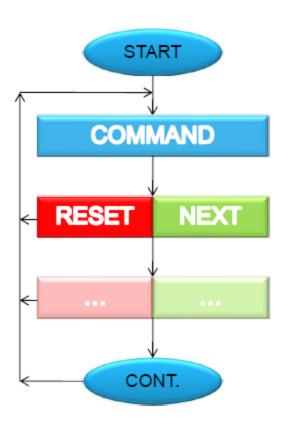
• Each state is configured through the Operation Codes (OPCODE). OPCODES can be divided in two groups:

NEXT/RESET CONDITIONS:

- RESET condition is in MSB part and NEXT condition is in LSB part of the OPCODE stored in configuration registers.
- RESET condition is evaluated first, NEXT condition is evaluated only if RESET was not valid
- Transition to next state happens when "NEXT condition" is valid
- Transition to reset point happens when "RESET condition" is valid
- If both conditions are not valid, the same conditions are applied to the next sample

COMMANDS:

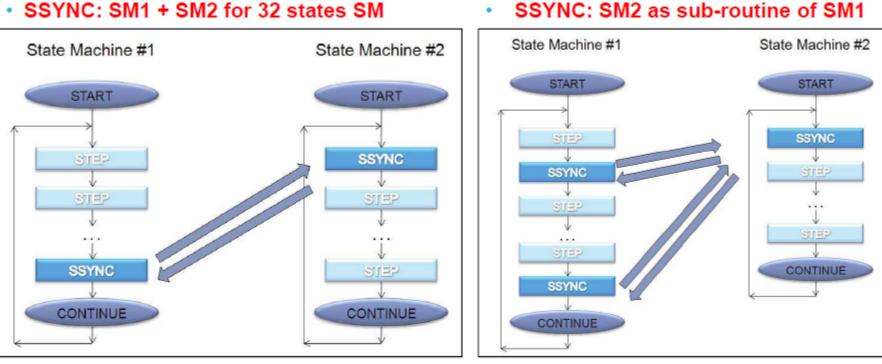
- Commands have special tasks for flow control, output and synchronization
- · Commands and their parameters are executed as one step command.





- Sequential synchronization (SSYNC command):
- Each state machine has 16 states that can be combined up to 32 sequential states
- State machine B can act as sub-function for State machine A (parameters can be totally different than main program)
- Host can change inactive State Program when other State Program is running
- State Program A can toggles execution to State Program B and viceversa





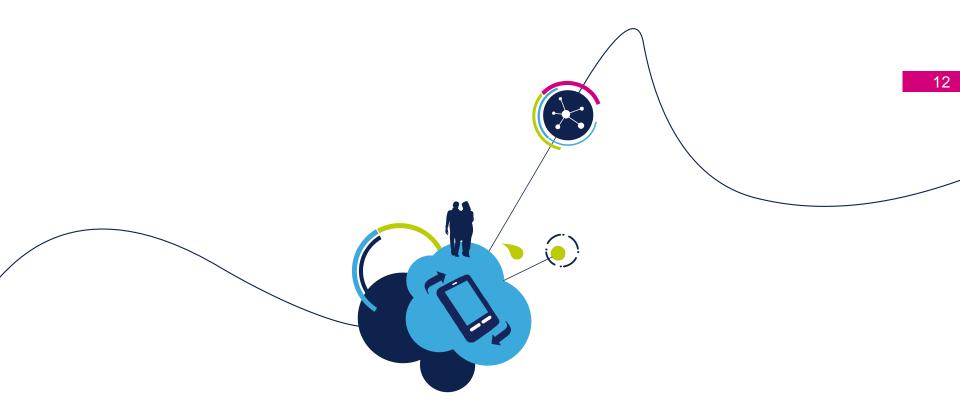
SSYNC: SM1 + SM2 for 32 states SM



State Machine Parameters 11

- 4 independent Timers
- 2 independent Masks (x, y, z, v)
- 3 independent acceleration Thresholds @8bit (Signed, Unsigned)
- Peak Detection function





Unico State Machine programs development



Unico 13

State Machine button

1	State Machine #1		State Machine #2		
	50 (0+40) . OPC . CMD	GNTH1 V TI1 V 0x 51	50 (0x50) O OPC (O OMD (S)	TOP 💌 0x 00	State Machine
	51 (0:41) @ OPC O DMD	GNTH1 V TH V 0x 51	ST (DAST) O OPC (O CMD ST	TOP 💉 🛛	Manage State Machine functions
	52 (0x42) . OPC . CMD		S2 (DWSZ) O OPC () CMD S	TOP 🔽 0x 00	Bead
	53 (0:43) OPC CMO	TI3 VINTH2 V 0x 28	53 (0x53) O OPC (CMD S	TOP 🖌 🖉 🖉	
	54 (0:44) OPC O DMD	NOP 👽 TI4 🐱 0x 04	54 (0x54) O OPC O CMD S	TOP 🖌 🖌 🛛	White
	55 (0:45) 💿 DPC 🔿 DMD	GTTH1 V TI1 V 0x 91	55 (0x65) O OPC () CMD ()	TOP 💽 0x 00	Visualize
	56 (0:46) ③ DPC 〇 CMD	TI2 GNTH2 0x 26	56 (DWSS) O OPC (O CMD) S	ТОР 🔽 🕅 🔟	Examples
	57 (0:47)	TI3 VINTH2 V 0x 28	57 (0467) O OPC O CMD	TOP 😽 0x 00	Open and charge State Machine
	58 (0#48)	NOP 🗙 TI4 🐱 0x 04	58 (0.68) O OPC O CMD S	TOP 🗸 0x 00	[STM-Ex] Double Tap.upm
	59 (0+49) 💿 DPC 🔾 CMD	GTTH1 V TI1 V 0x S1	59 (0469) 🔾 OPC 💽 CMD 🛛 🗍	TOP 💉 0x 00	
	S10 (0:4A) O DPC 💿 CMD	CONT 🐱 0x 11	\$10 (0.64) O OPC () CMD ()	TOP 💉 0x 00	Load
	S11 (0:48) OPC @ CMD	STOP 😼 0x 00	STT (0.68) O DPC (CMD)	TOP 😽 0x 00	Save as Example
	\$12 (0:4C) O OPC O CMO	STOP 😪 0x 00	\$12 (0x6C) O OPC () CMD ()	TOP 😽 0x 00	Pattern
	\$13 (0:4D) O DPC 💿 CMD	5TOP 💌 0x 00	513 (0460) O OPC () OMD 5	TOP 💉 0x 00	Load Data patterm
	514 (0:4E) O OPC @ CMO	STOP 🖌 😽 🛛	S14 (0x6E) O OPC (OMD 5	TOP 💉 00	[STM-PT]Es2upd
	515 (0/4F) O DPC O DMD	5TOP 🗸 0x 00	S15 IGASEL O OPC O OND 51	TOP 🔽 0x 00	Load

Each state can be programmed selecting either the code or through the interface



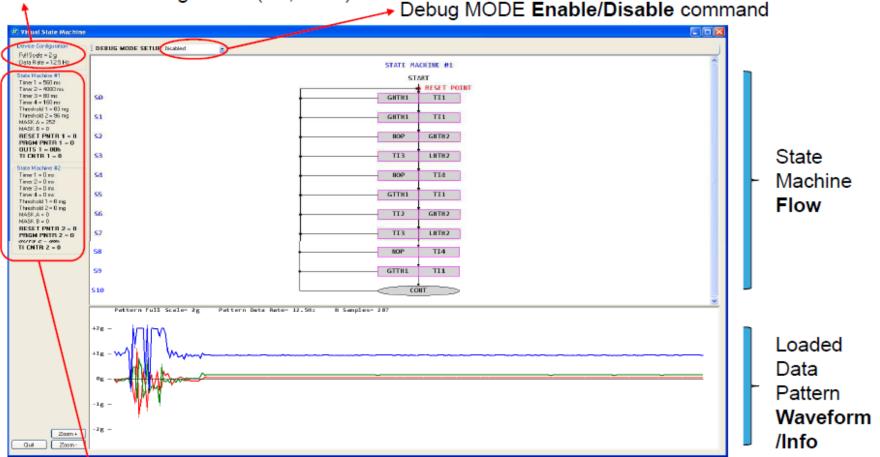
Read, Write and Visualize the current state machine

Load or Save State Machine Configuration

Load Data Pattern to test the State Machine

Unico – Debug mode DISABLED 14

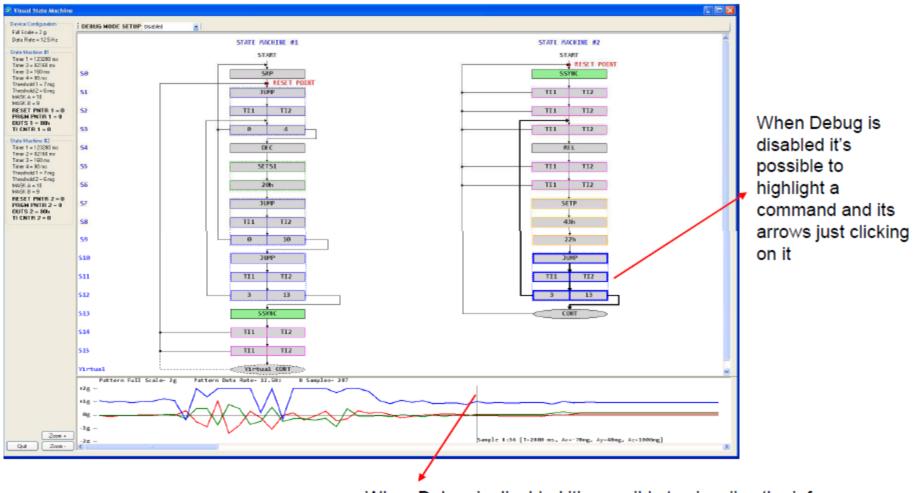
Actual device's configuration (FS, ODR)



NOTE: State machines parameters are reported with Unit of Measurement indicated: their values are related to both corresponding registers value and device's FS/ODR in use.



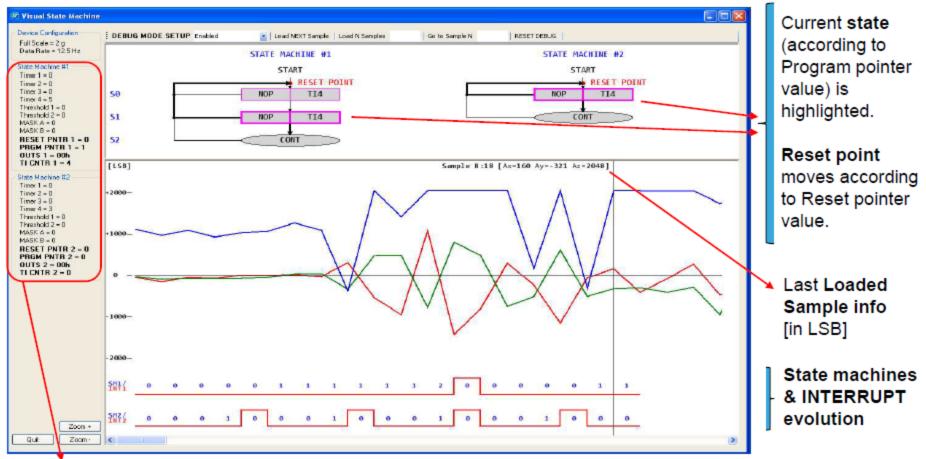
Unico – Debug mode DISABLED 15



When Debug is disabled it's possible to visualize the info of each sample just clicking on it



Unico – Debug mode ENABLED 16



NOTE 1: Units of Measurement are meaningless when Debug mode is Enabled; State machines parameters are related only to registers' values.

NOTE 2: Boldfaced parameters are REAL TIME updated.

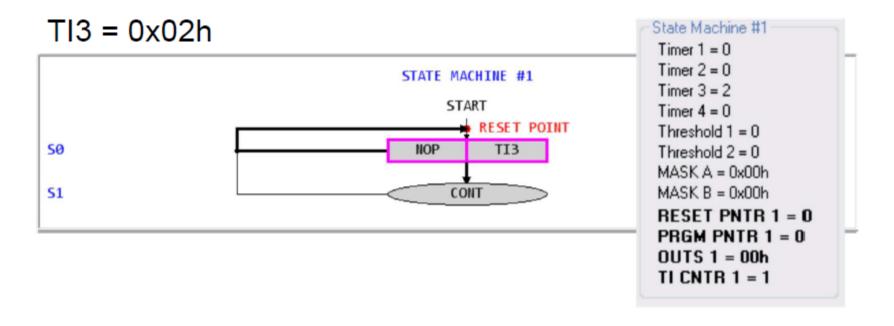


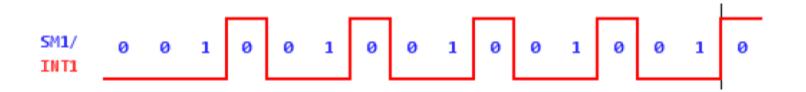
Examples 17

- 1. Toggle
- 2. Wake Up
- 3. Free Fall
- 4. Double Tap
- 5. SSYNC



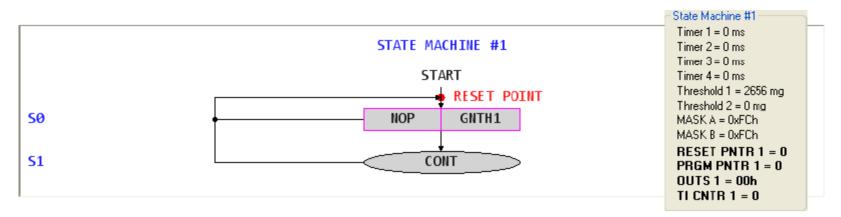
Toggle 18

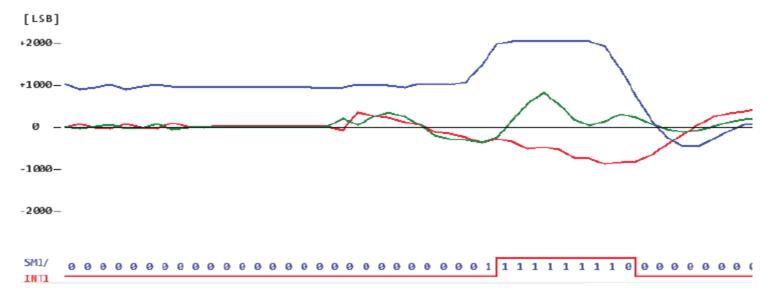






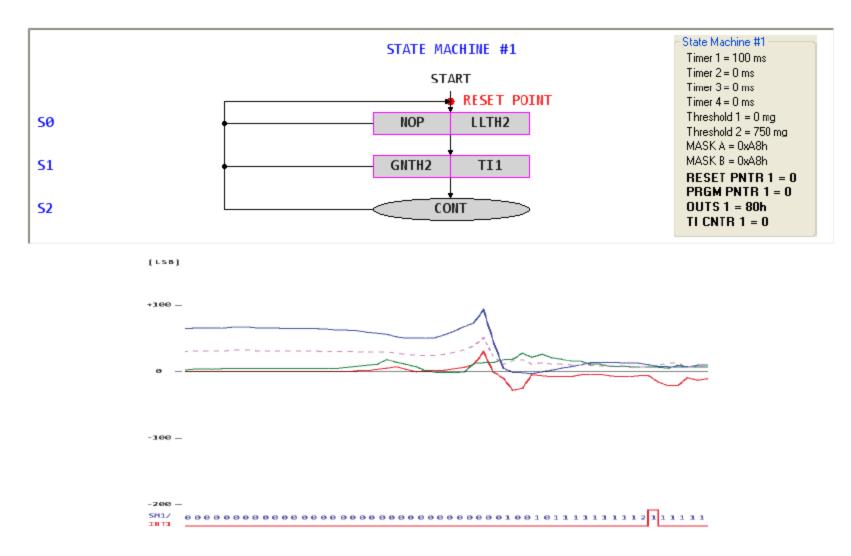
Wake up





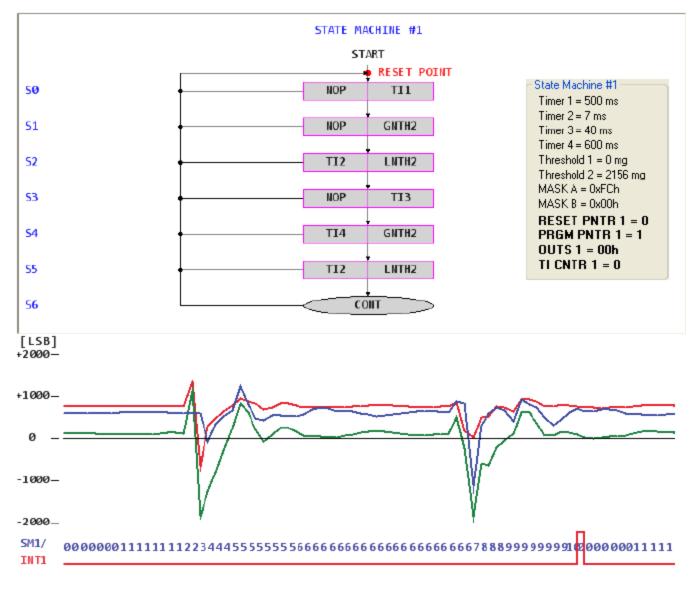


Free Fall 20



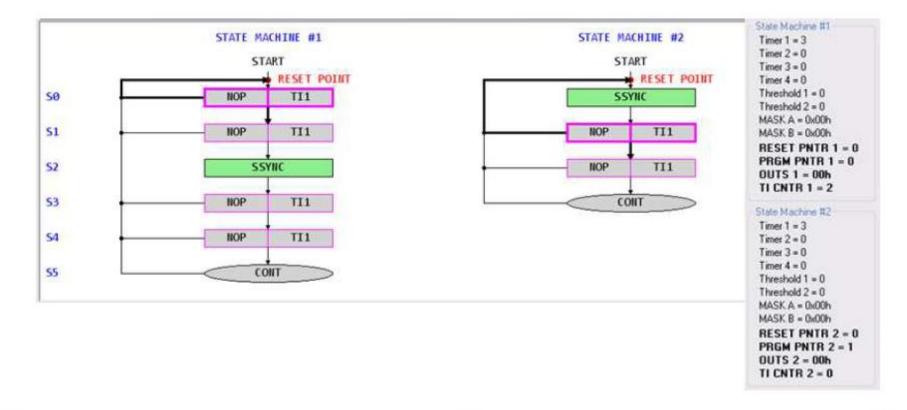


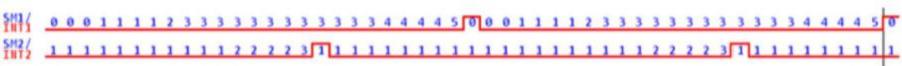
Double Tap 21





SSYNC 22







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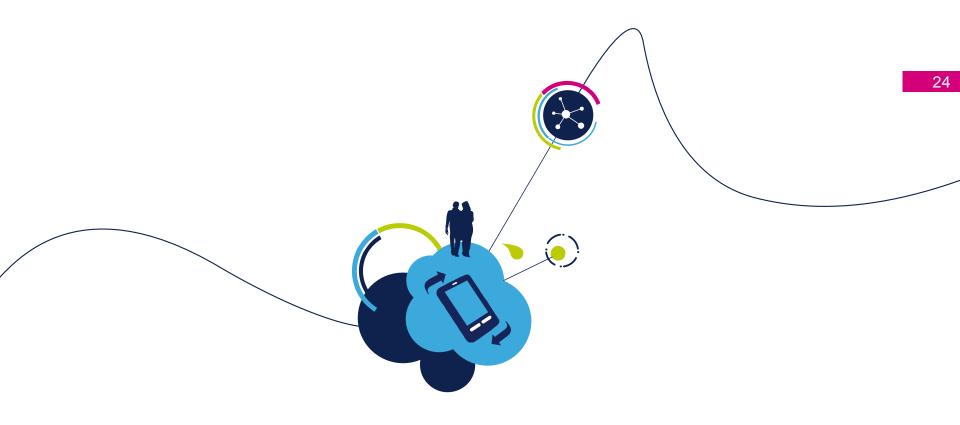




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Thank you !

